JC855 U.S. PTO
09/998859
11/16/01

| | Subclass | ISSUE CLASSIFICATION |
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| | Class | |

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PATENT NUMBER

# U.S. **UTILITY** Patent Application

| O.I.P.E. | PATENT DATE |
|---|---|
| SCANNED TK2 Q.A. MT | |

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
|---|---|---|---|---|---|
| 09/998859 | | 716 | 4 | 2825 | Tr ... |

APPLICANTS: Craig Nemecek

TITLE: System and a method for checking lock step consistency between an in circuit emulation and a microcontroller while debugging process is in progress

PTO-2040
12/99

## ISSUING CLASSIFICATION

| ORIGINAL | | CROSS REFERENCE(S) | |
|---|---|---|---|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) |
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| INTERNATIONAL CLASSIFICATION | | | |
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| | | | ☐ Continued on Issue Slip inside File Jacket |

| ☐ TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|---|---|---|---|---|---|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| ☐ The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) | | _____ (Date) | NOTICE OF ALLOWANCE MAILED | |
| ☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ | _____ (Primary Examiner) | | _____ (Date) | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| ☐ The terminal ___months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) | | _____ (Date) | ISSUE BATCH NUMBER | |

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